

ABSTRACT OF THE DISCLOSURE

A switched capacitor amplifier circuit has a pair of input terminals, a pair of capacitors connected to respective input terminals, and a pair of switch circuits connected to respective capacitors and input terminals. Each of a pair of reference voltage terminals receives a reference voltage for supplying electric charges to a respective one of the capacitors. The reference voltage terminals are connected to respective switch circuits so that noise components of the reference voltages are in phase to reduce noise during adjustment of an offset voltage between the input terminals.